

Electronic Patent Application Fee Transmittal

Application Number:	10811835			
Filing Date:	30-Mar-2004			
Title of Invention:	Method of designing a circuit layout of a semiconductor device			
First Named Inventor/Applicant Name:	Kazuaki Goto			
Filer:	Donald R. Studebaker/Pamela Patrick			
Attorney Docket Number:	030712-29			
Filed as Large Entity				
Utility under 35 USC 111(a) Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Filing a brief in support of an appeal	1402	1	540	540
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension - 2 months with \$0 paid	1252	1	490	490
Miscellaneous:				
Total in USD (\$)				1030